

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re PATENT APPLICATION of:

Robert L. FRANCH et al.

Application No.: 10/712,925

Art Unit: 2833

Filed: November 13, 2003

Examiner: Vit T.MISKA

For: BUILT IN SELF TEST CIRCUIT FOR
MEASURING TOTAL TIMING
UNCERTAINTY IN A SYNCHRONOUS
DATA PATH

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to a Restriction Requirement dated April 13, 2007, Applicants hereby elect Group I, claims 1 – 30 drawn to a circuit for measuring timing uncertainties without traverse. Should the Examiner believe anything further may be required, the Examiner is requested to contact the Applicant's undersigned attorney at the local telephone number listed below for a telephonic or personal interview to discuss any other changes.

Respectfully Submitted,

April 26, 2007
(Date)

/Charles W. Peterson, Jr, #34,406/
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